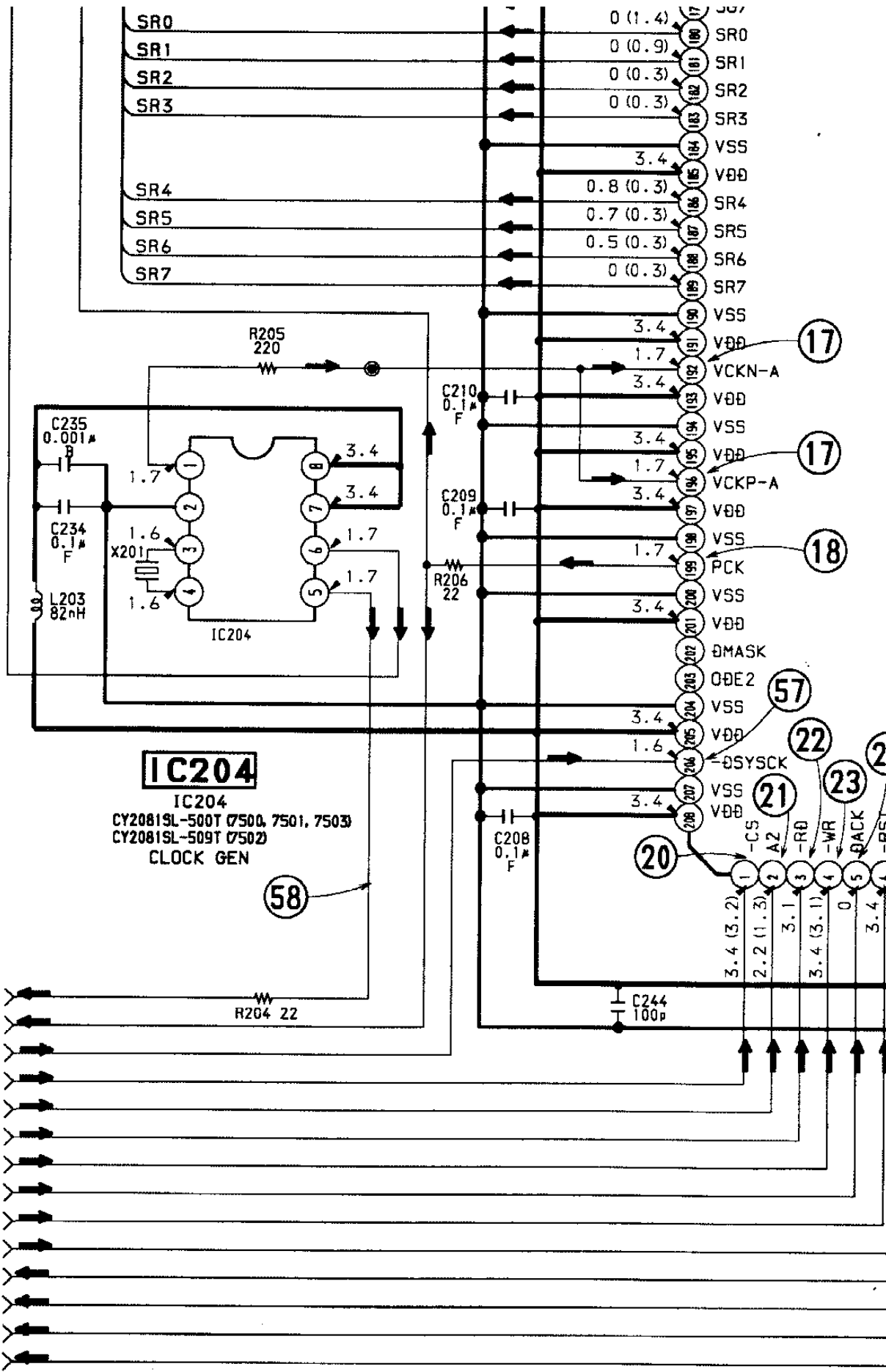


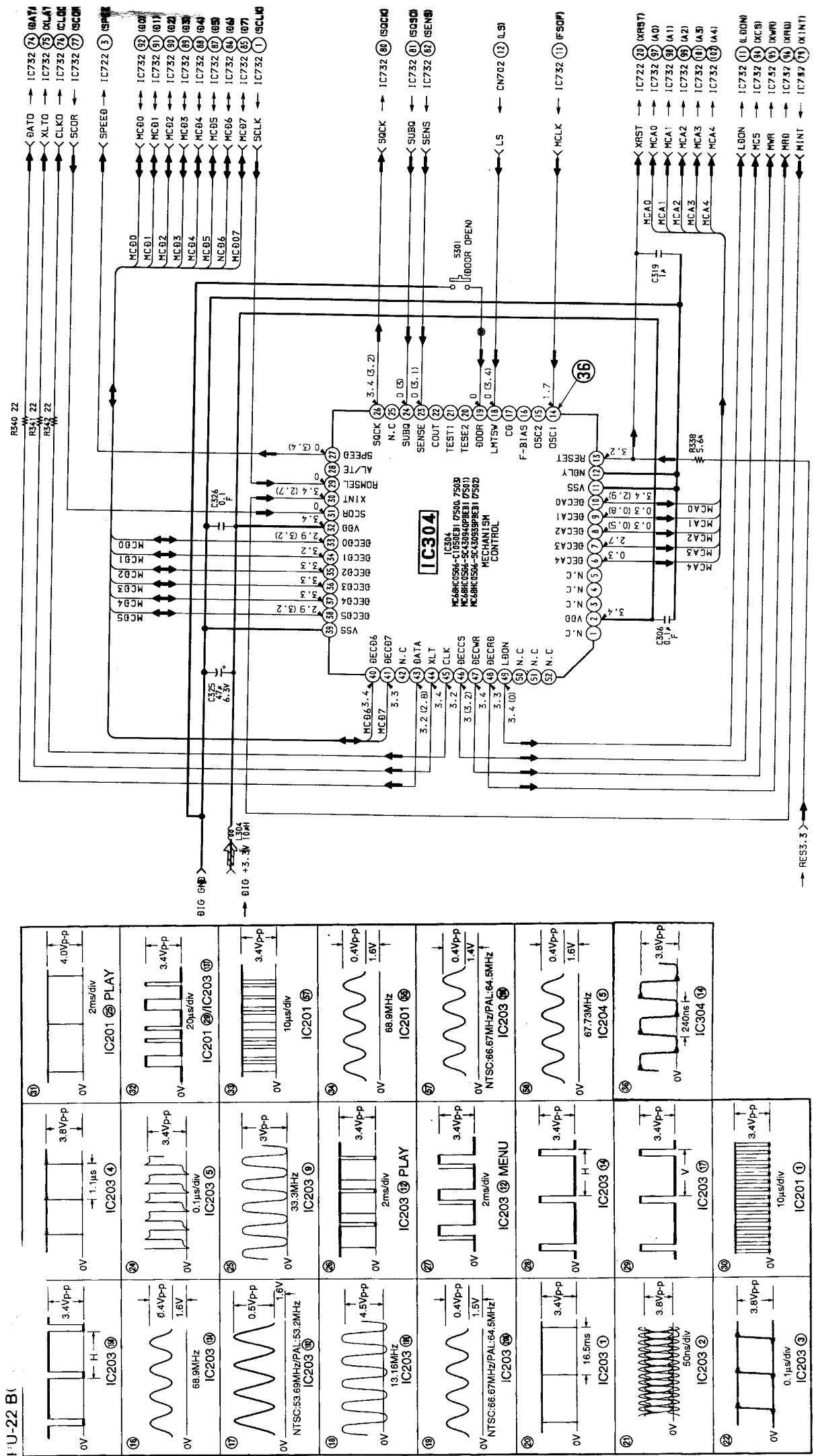
X201
 14.32MHz (7500, 7501, 7503)
 17.73MHz (7502)

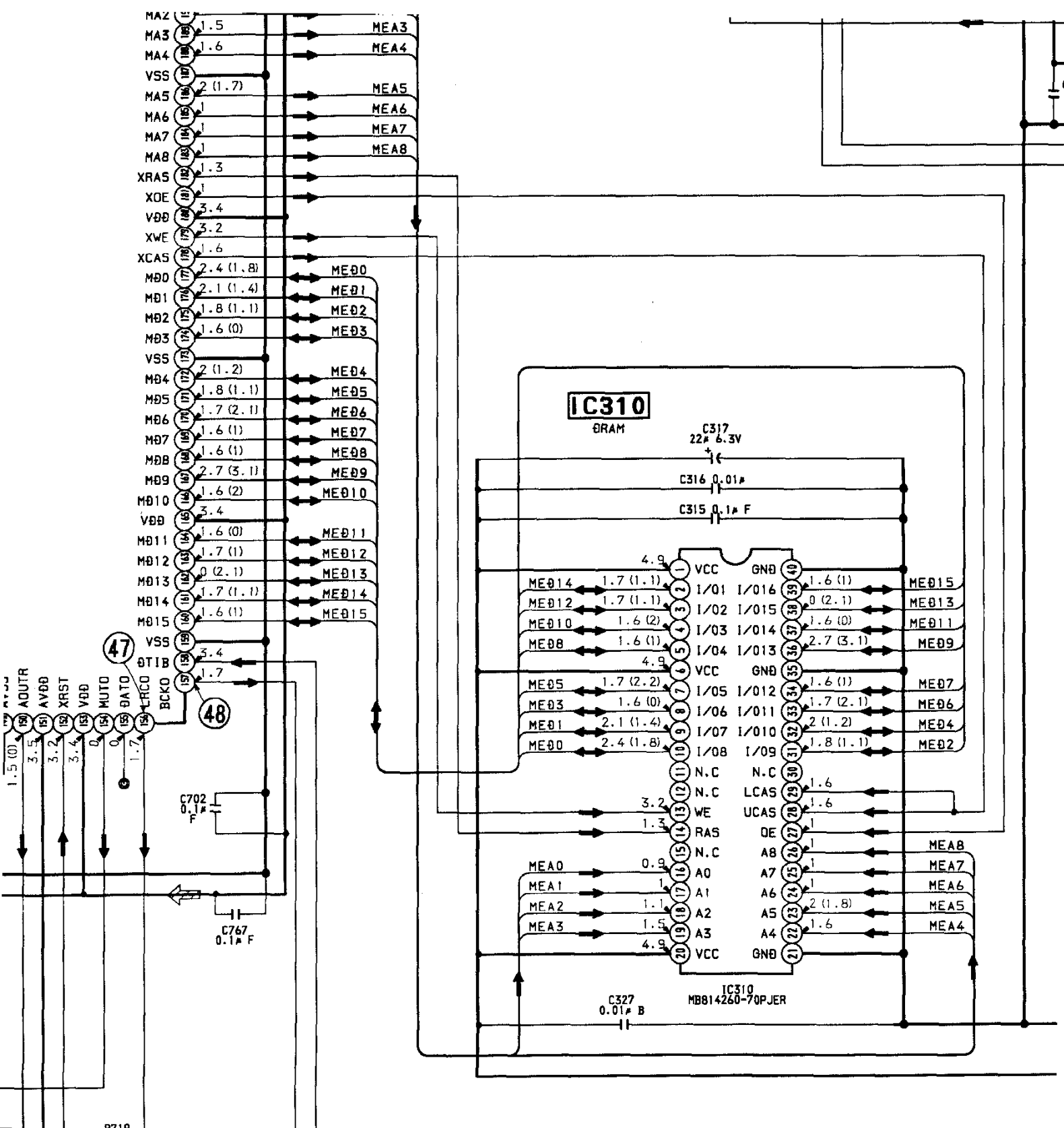


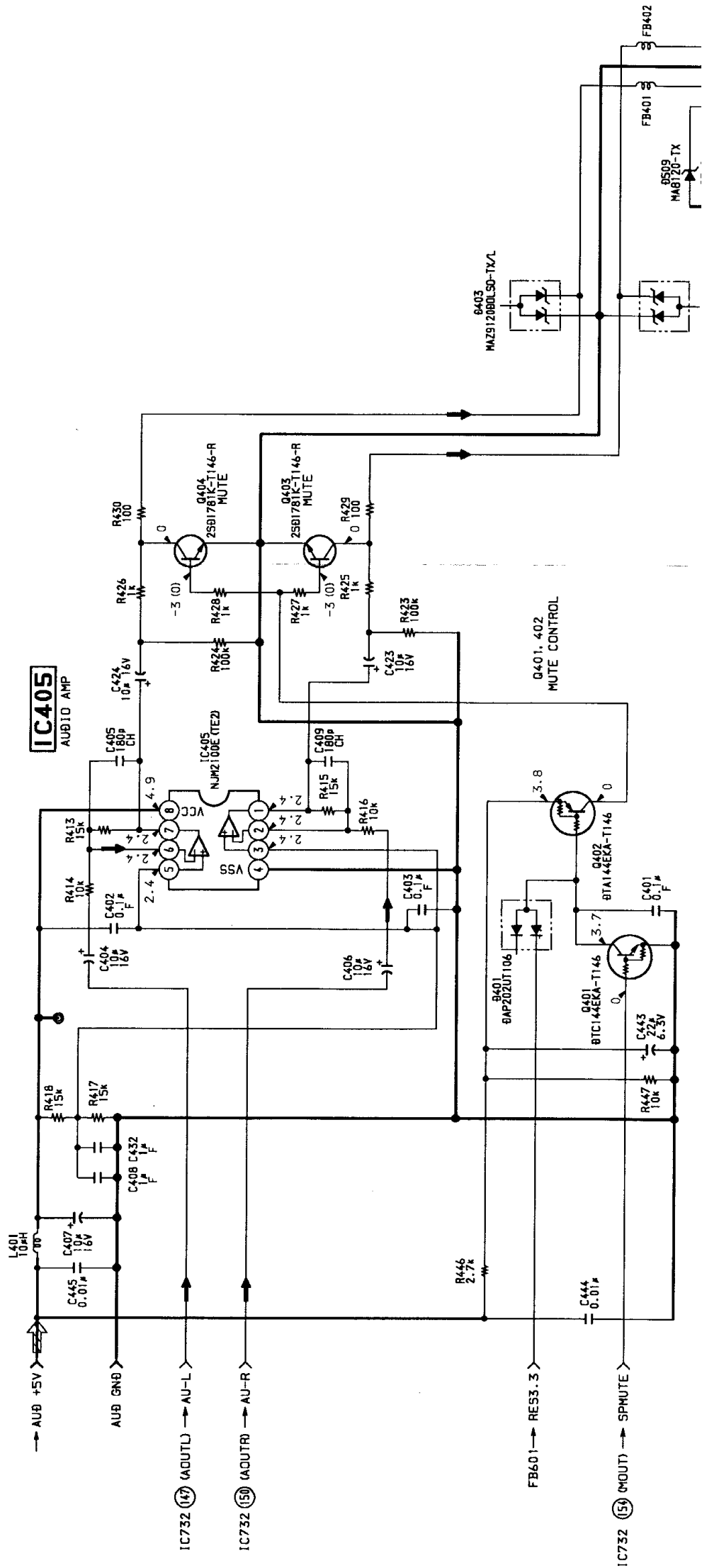
- ④ (CRYSTALP) ← CPUCLK >
- ①⑥⑩ (TL CLO) ← PCK >
- ⑦⑩④ (SYSCLK) → DBLCLK >
- ②⑤⑦ (CS7) → GPUCS >
- ⑤③ (VA2) → GPU A2 >
- ②④ (VRD) → GPU RD >
- ②③ (VWR) → GPU WR >
- ②② (DACK2) → GPU DACK >
- ⑦⑥ (EXT RESET) → RES3.3G >
- ②① (SYSCLK0) → SYSCLK0 >
- ②① (DREQ2) ← GPU DREQ >
- ①② (INTIN1) ← GPU INT >
- ①⑤⑨ (TCLK1) ← HBLANK >
- ①⑨⑨ (INTINO) ← VBLANK >

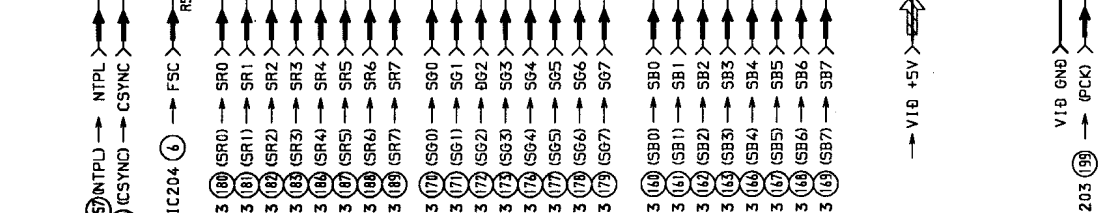
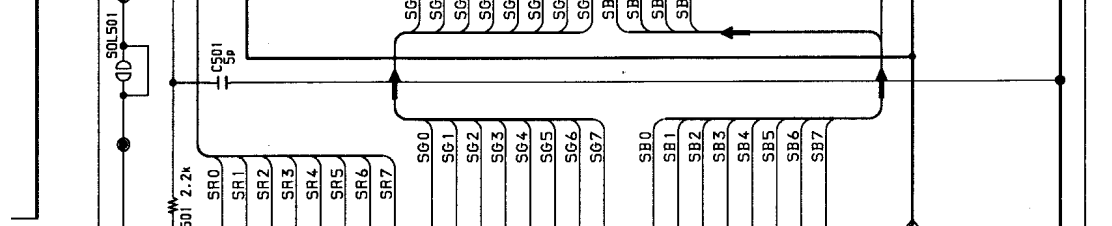
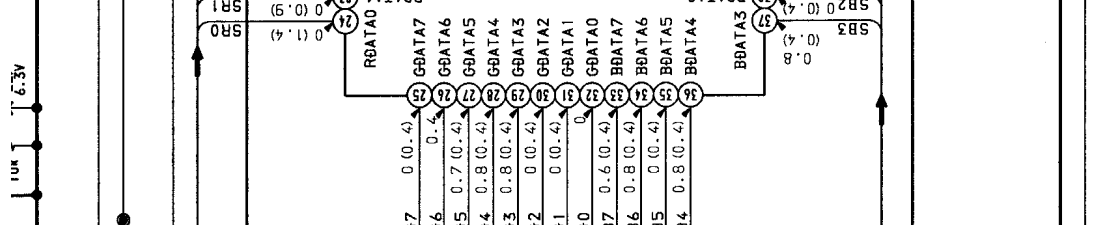
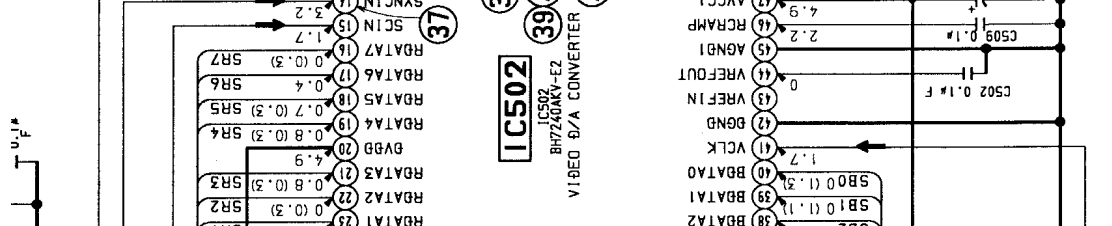
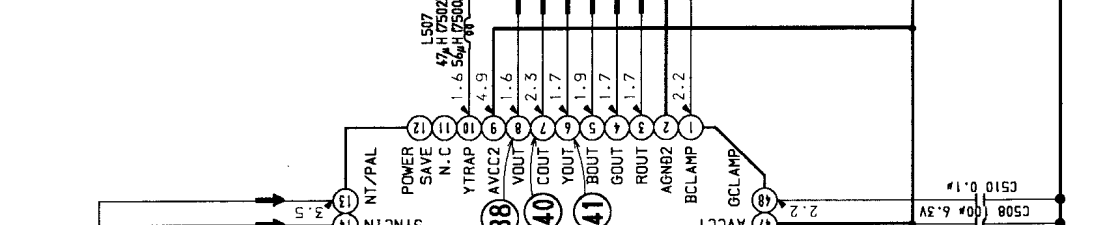
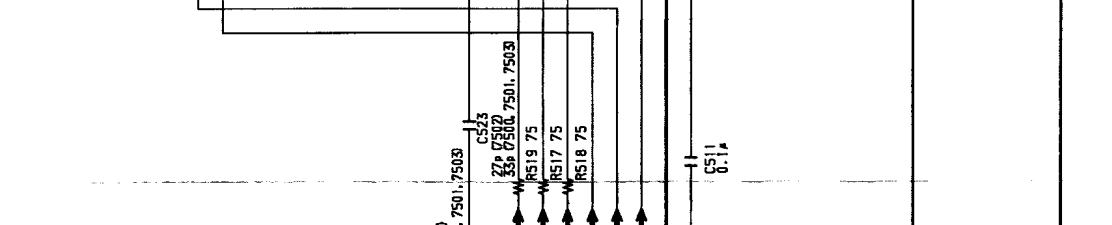
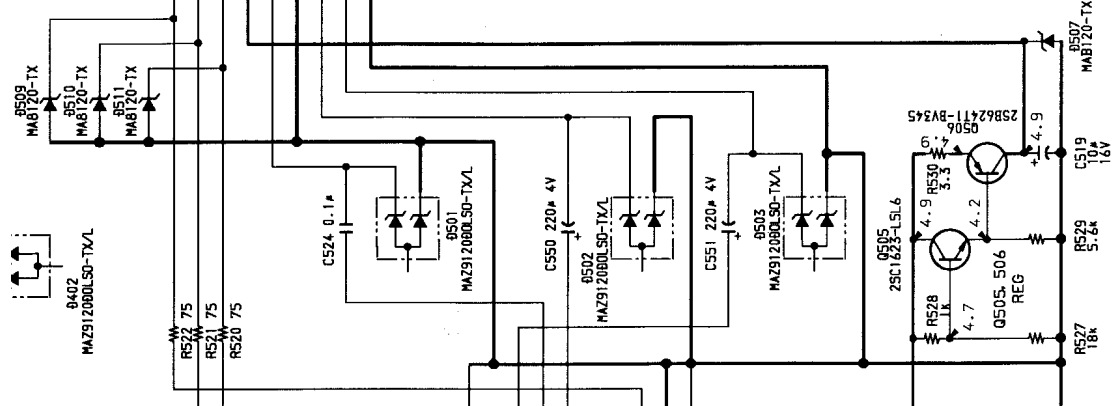
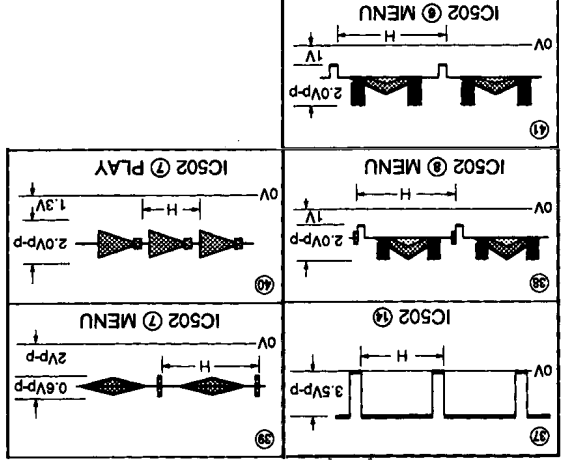
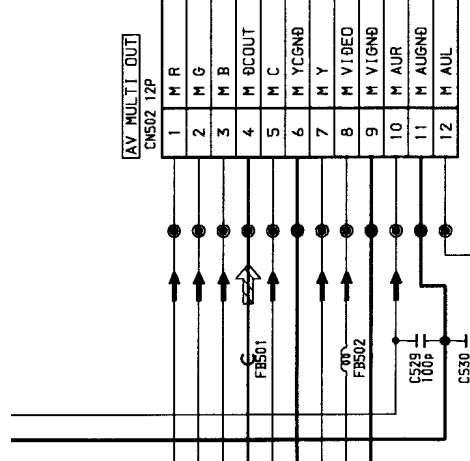
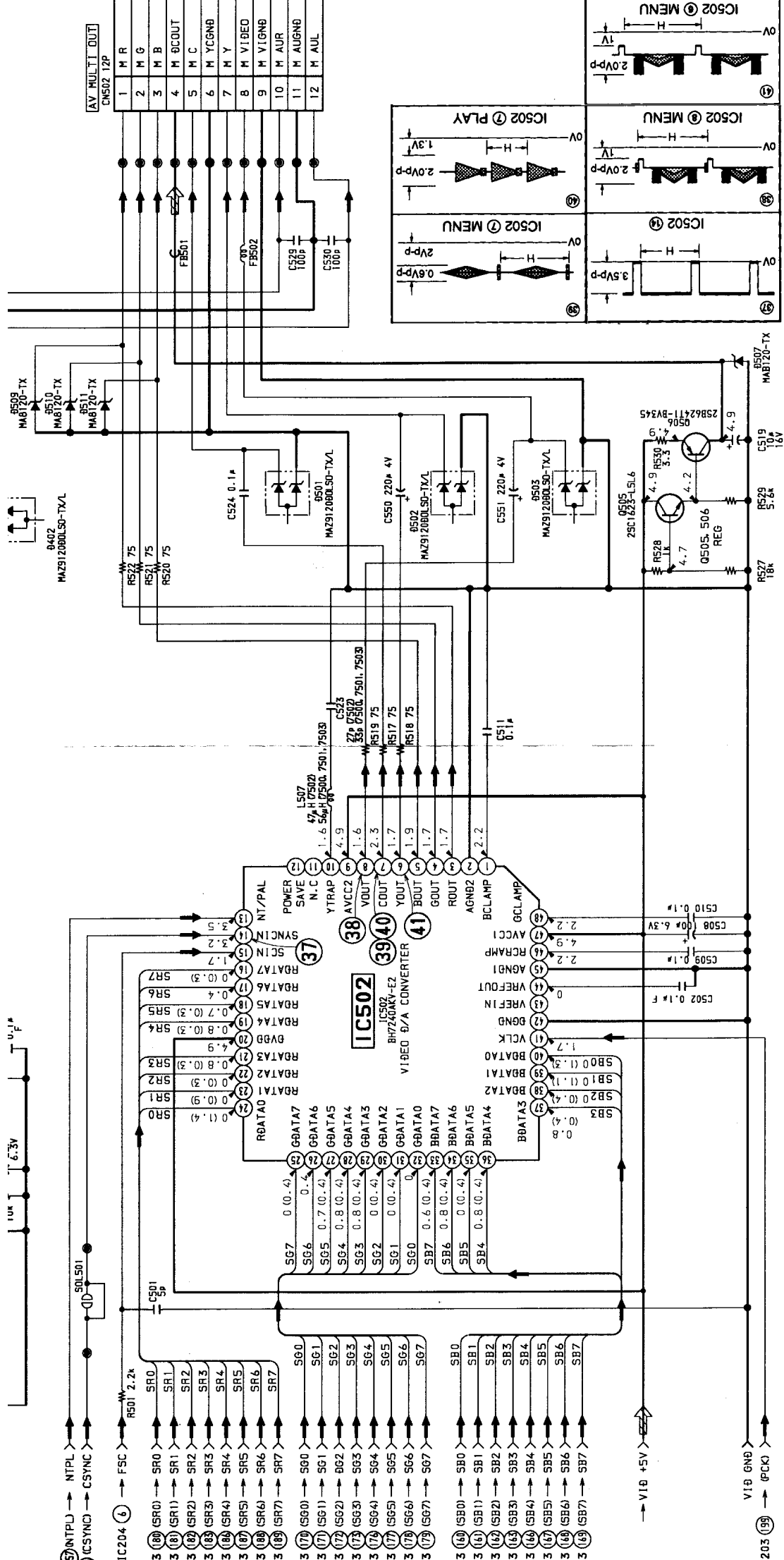
6-5. SCHEMATIC DIAGRAM (PU-22 (-11-12-21-22-32) BOARD (3/5))

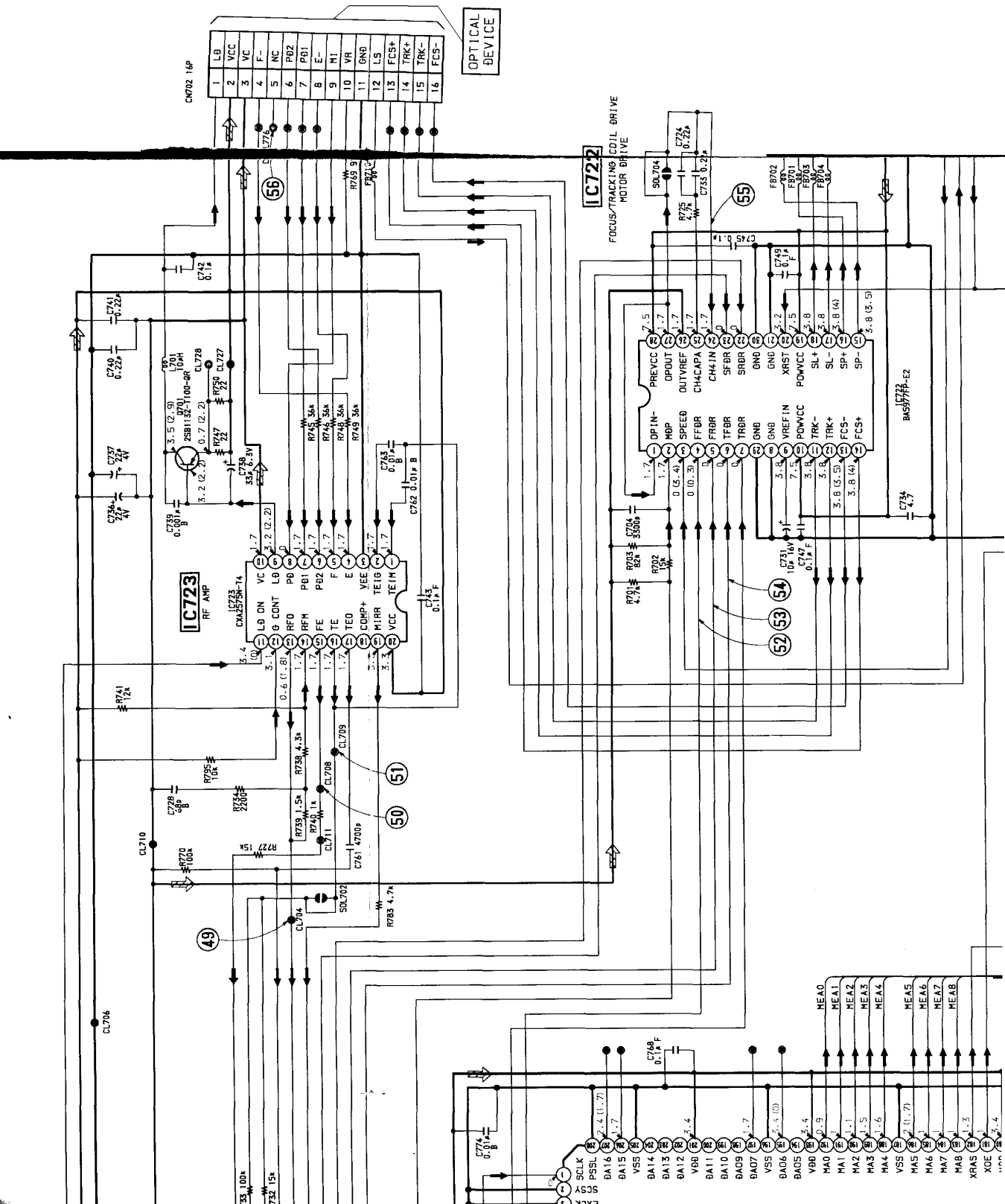
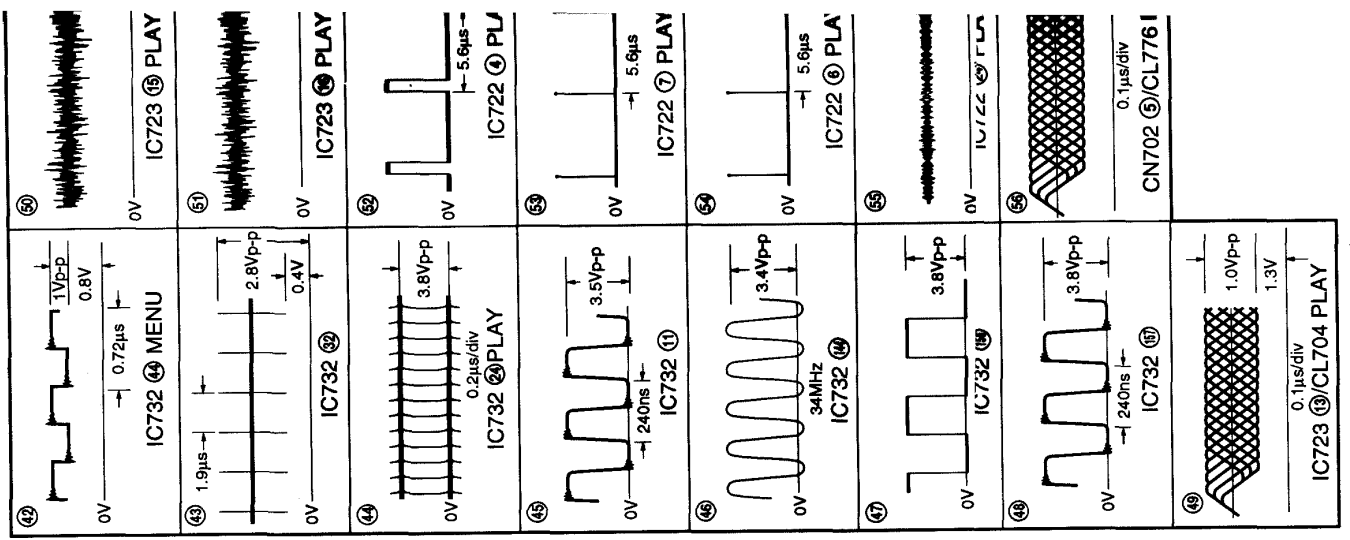
PU-22 BOARD (3/5)

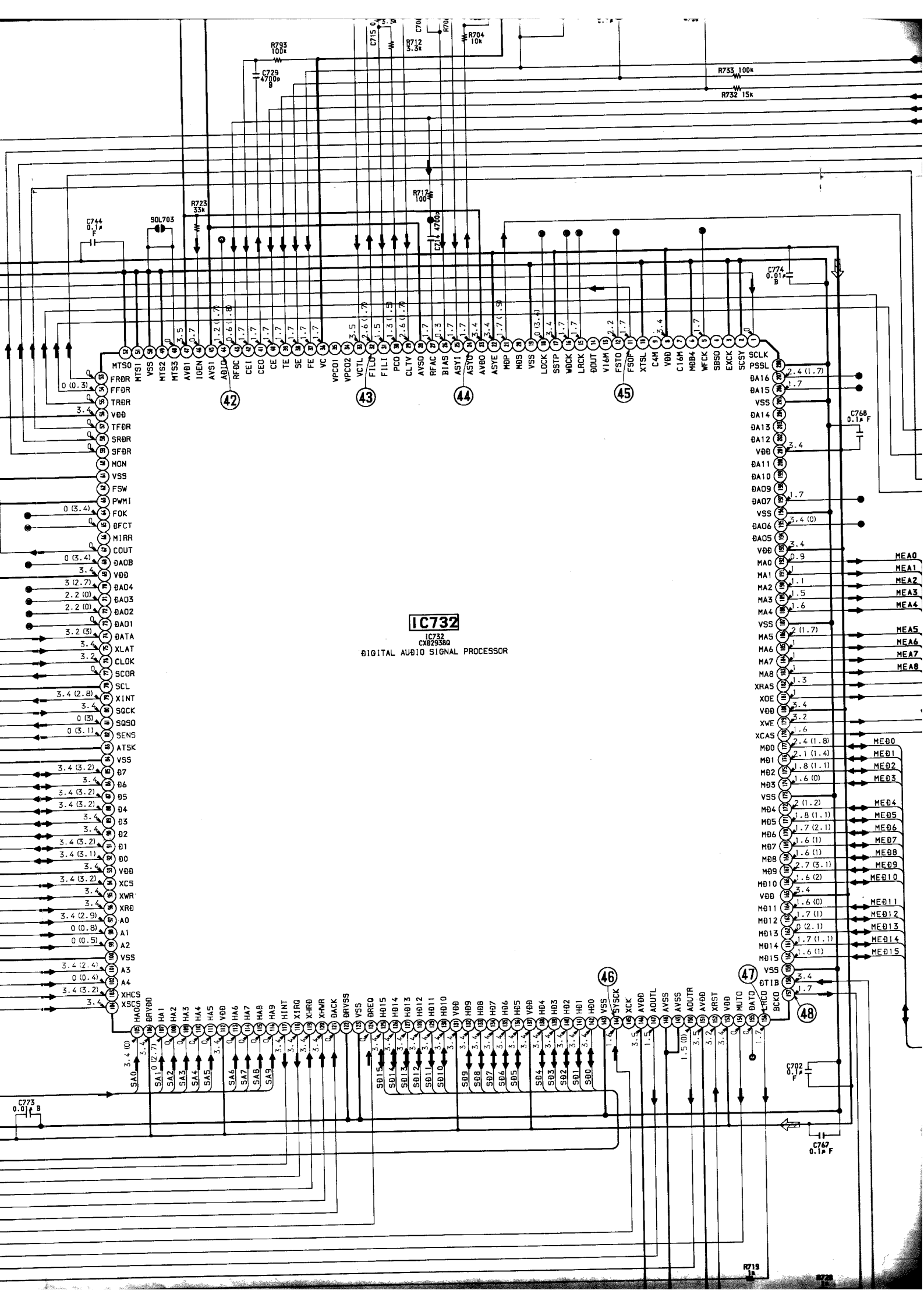












IC732

IC732
CX829380
DIGITAL AUDIO SIGNAL PROCESSOR

42

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46

47

48

MEA0
MEA1
MEA2
MEA3
MEA4
MEA5
MEA6
MEA7

ME00
ME01
ME02
ME03
ME04
ME05
ME06
ME07
ME08
ME09
ME010
ME011
ME012
ME013
ME014
ME015

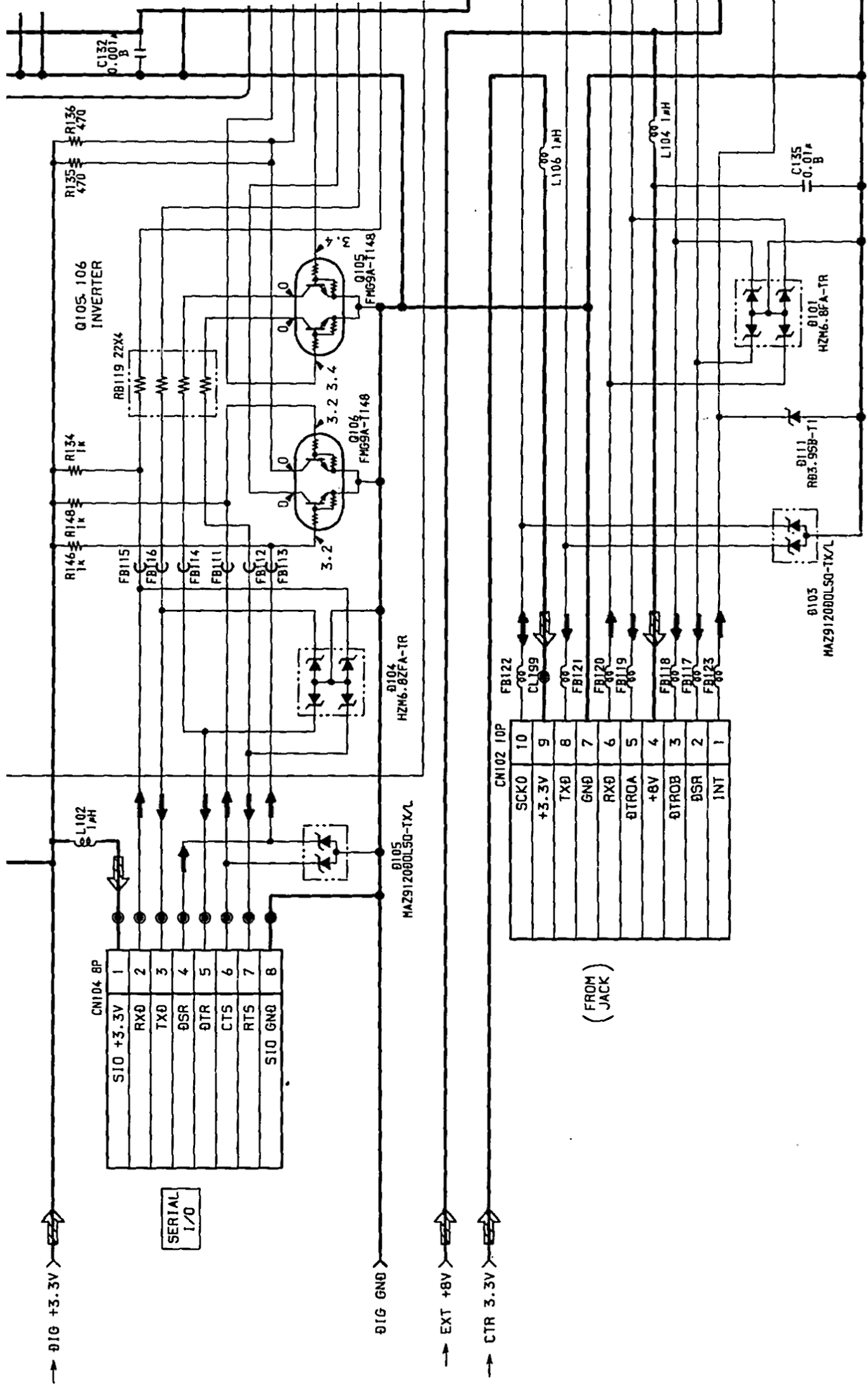
C702
0.1µF

C767
0.1µF

C773
0.01µF

R719
1k

R720



SERIAL I/O

CNI04.8P	
1	SIO +3.3V
2	RXD
3	TXD
4	DSR
5	QTR
6	CTS
7	RTS
8	SIO GND

CNI02.10P	
10	SCK0
9	+3.3V
8	TX0
7	GND
6	RX0
5	QTR0A
4	+8V
3	QTR0B
2	DSR
1	INT

(FROM JACK)

→ 01G +3.3V

→ 01G GND

→ EXT +8V

→ CTR 3.3V

0105
MAZ912080LS0-TX/L

0111
R03.95B-T1

0101
HZM6.8ZFA-TR

C135
0.001µ

R135
470

R136
470

C132
0.001µ

L104 1µH

L106 1µH

R146 1K

R148 1K

R134 1K

R03.95B-T1

0104
HZM6.8ZFA-TR

L102 1µH

0105
FM99A-T148

0104
HZM6.8ZFA-TR

0105
MAZ912080LS0-TX/L

0105
FM99A-T148

0104
FM99A-T148

0104
HZM6.8ZFA-TR

0105
MAZ912080LS0-TX/L

0104
FM99A-T148

0104
HZM6.8ZFA-TR

L102 1µH

0105
FM99A-T148

0104
HZM6.8ZFA-TR

L102 1µH

0105
FM99A-T148

0104
HZM6.8ZFA-TR

L102 1µH

0105
FM99A-T148

0104
HZM6.8ZFA-TR

L102 1µH

0105
FM99A-T148

0104
HZM6.8ZFA-TR

L102 1µH

0105
FM99A-T148

0104
HZM6.8ZFA-TR

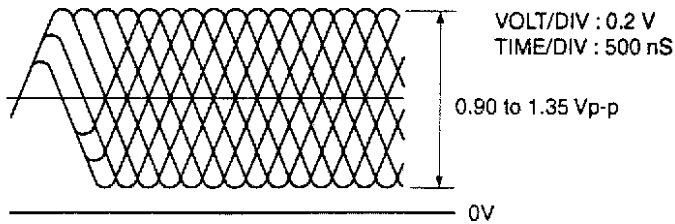
L102 1µH

SECTION 3 ADJUSTMENTS

3-1. CHECK SPECIFICATION

RF level 0.90 to 1.35 Vp-p (Check point : Between CL704 (HOT) and CL710 (VC).)

• RF signal waveform (eye pattern)



Use SCD-2700 DISC when measured RF level.
Use the oscilloscope with input impedance more than 10 MΩ.

RF Jitter Below 9.0 nS (Measuring by KJM-6135S JITTER METER.)

Below 27.0 nS (Measuring by KJM-6235S JITTER METER.)

PP level 1.1 ± 0.6 Vp-p (Check point : Between CL776 (HOT) and CL710 (VC).)

Use LPF (fc = 10 kHz)

Tracking level 1.25 ± 0.65 Vp-p (Check point : Between CL709 (HOT) and CL710 (VC).)

Caution. Vc Line (CL710) do not make common use with GND line.

Check Point for PU-22 Board.

